

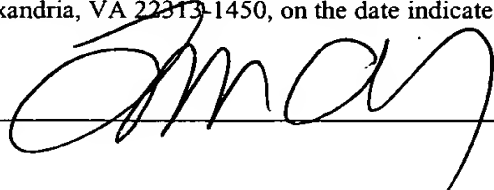
OTIP E JC106
JUL 25 2003
PATENT & TRADEMARK OFFICE

1752814
ce \$

Docket No.: GR 98 P 2499

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

By:



Date: July 23, 2003

#16/002
Supple
Response
RAG
8/12/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Peter Haneder et al.
Applic. No. : 09/801,209
Filed : March 7, 2001
Title : Ferroelectric Transistor, Use Thereof in a Memory Cell Configuration and Method of Producing the Ferroelectric Transistor
Examiner : Thao X. Le - Art Unit: 2814

SUPPLEMENTAL RESPONSE
UNDER 37 C.F.R. 1.116

RECEIVED
JUL 31 2003
TECHNOLOGY CENTER 2800

Hon. Commissioner for Patents,

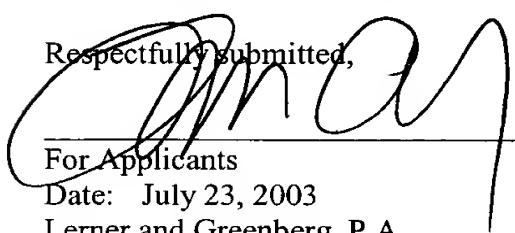
Sir:

Responsive to the final Office action dated February 25, 2003, and supplemental to the *Response under 37 C.F.R. 1.116*, filed on June 2, 2003, applicants herewith enclose a *Terminal Disclaimer*.

Petition for extension is herewith made. The extension fee for response within a period of two (2) months pursuant to Section 1.136(a) in the amount of \$410.00 in accordance with Section 1.17 is enclosed herewith.

Please charge any other fees which might be due with respect to Sections 1.16 and 1.17 to Deposit Account No. 12-1099 of Lerner and Greenberg, P.A..

Respectfully submitted,



LAURENCE A. GREENBERG
REG. NO. 29,308

For Applicants

Date: July 23, 2003
Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101
/bb